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This chapter provides an introduction to embedded multicore processors. A multicore processor consists of multiple central processing units (CPUs) residing in one physical package and interfaced to a motherboard. Using multicore processors can result in faster execution time, increased throughput, and lower power usage for embedded applications.

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need to increase performance while conserving power, has placed great responsibility on the shoulders of software engineers. In this new embedded multicore era, the toughest task is the development of code to support more sophisticated systems.

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Several embedded software frameworks have been developed for multi-core processors, but more are needed for improved software development and performance. The frameworks discussed in the rest of this chapter are a sampling of available frameworks. The mention of each framework is not intended as a recommendation.

## ~~Software Development for Parallel and Multi-Core Processing~~

Bluefruit are embedded software specialists. For the past 20 years, Bluefruit Software has worked closely with its clients to deliver high-quality embedded software and firmware solutions across a range of sectors. We have a team of over 70 people based in our Cornwall office. This includes multi-disciplinary software development teams consisting of Embedded Software Engineers,

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## ~~Embedded Software Development | Embedded Product Development~~

In any case, all the above-mentioned tools help accelerate the development of embedded software. Our Embedded Experience. Embedded development is among the core competencies SaM Solutions offers to its clients. We have successfully completed dozens of projects, both for large and small companies. The range of end-to-end services we offer includes: Custom firmware development; Embedded software development; Linux device driver development

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A common problem with multi-core development is the proper synchronization of software execution. In this case, the embedded system design may wish to check the data traffic on the busses between the processor cores, which requires very low-level debugging, at signal/bus level, with a logic analyzer , for instance.

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